



FIG. 2

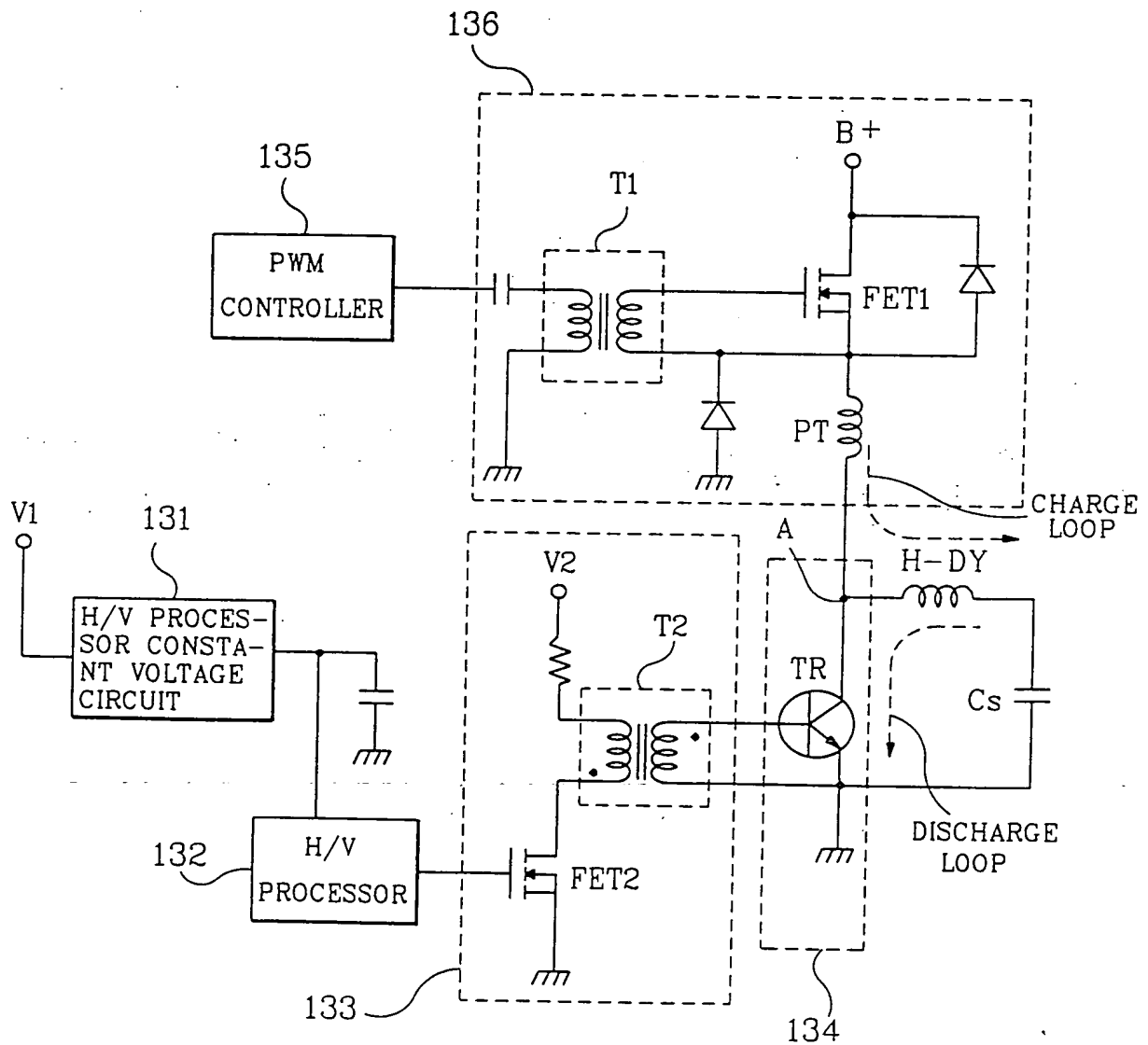


FIG. 3

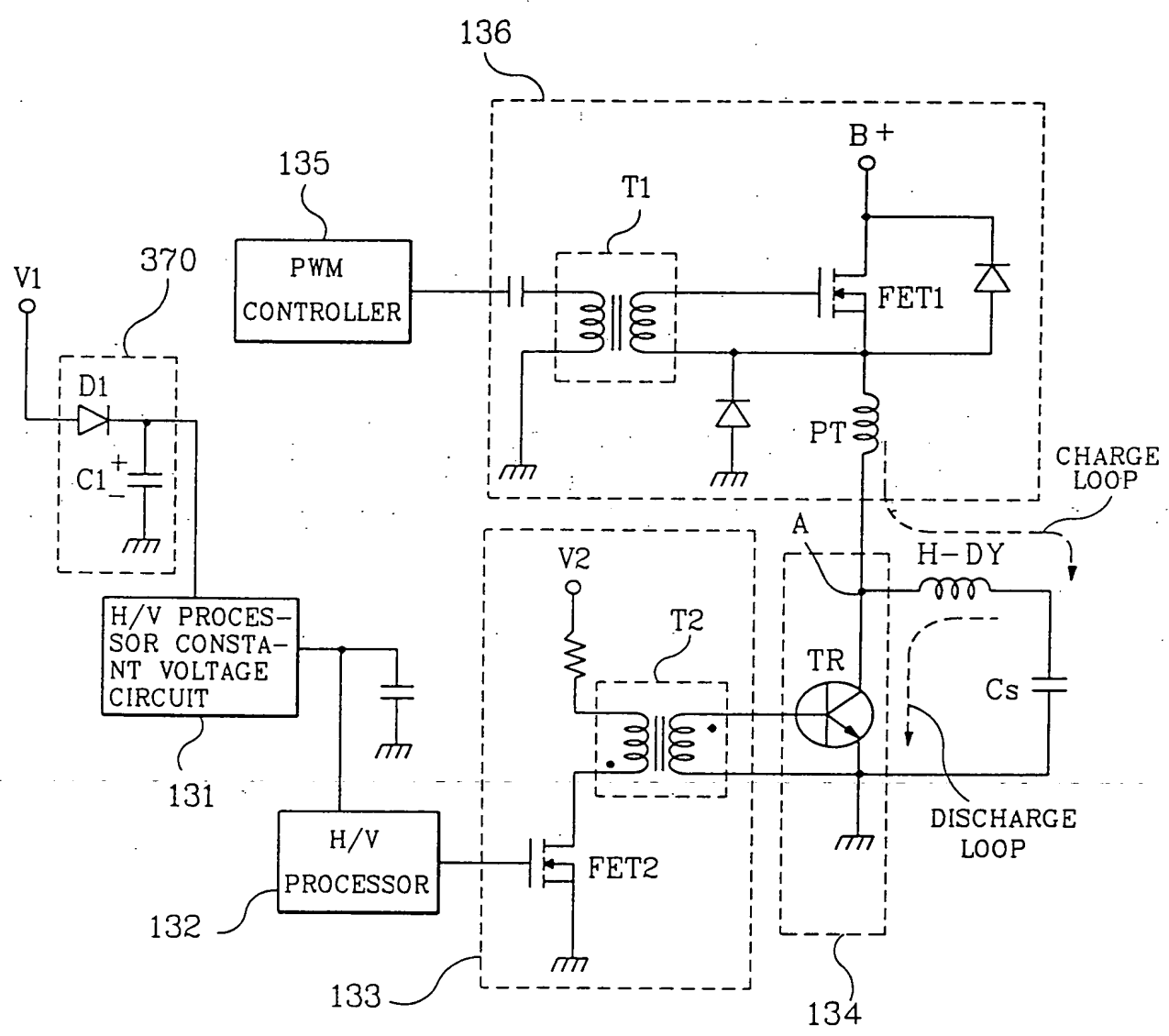


FIG. 4A

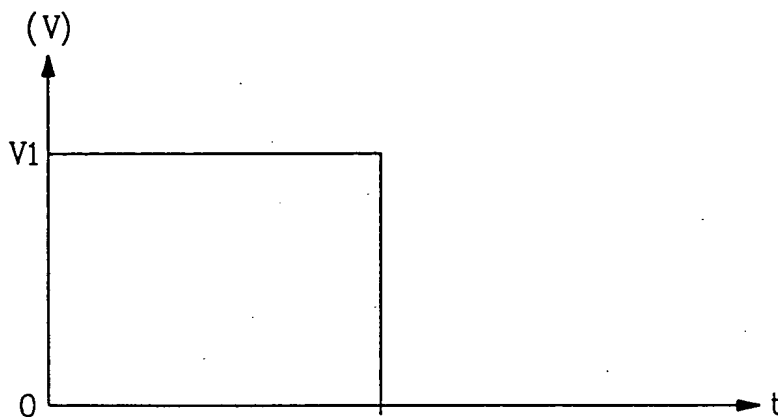
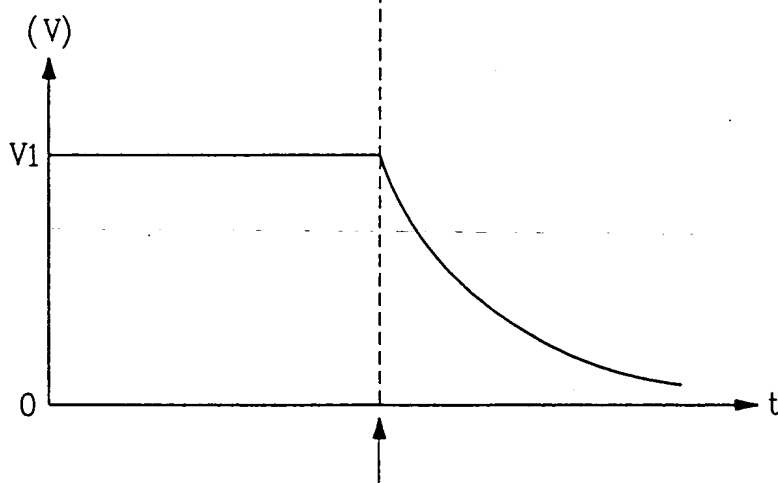


FIG. 4B



INTERRUPTION TIME POINT  
OF POWER SUPPLY TO  
DISPLAY DEVICE

FIG. 5A

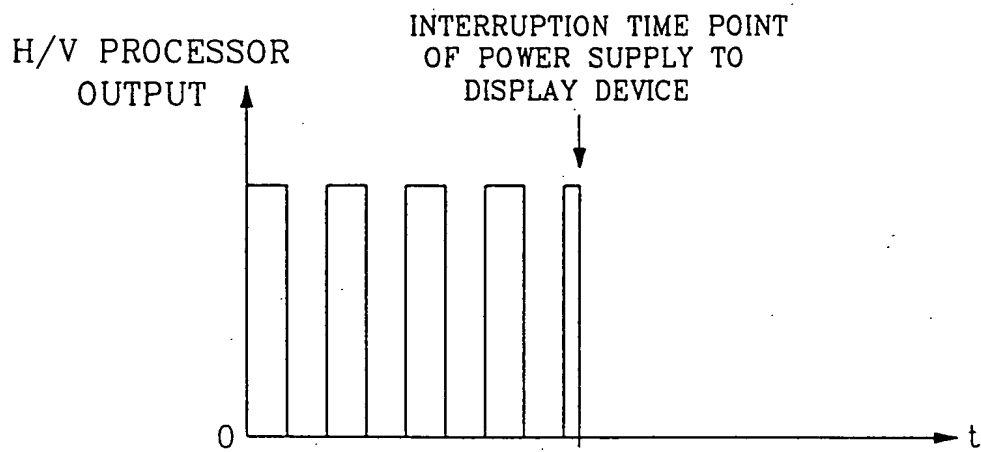


FIG. 5B

